



SUN-P5403

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#5/Amend a  
A Foral  
4/16/03

APPLICANT: Manjunath D. Haritsa et al.  
SERIAL NO.: 09/982,452  
FILING DATE: October 17, 2001  
TITLE: CLOCK SKEW VERIFICATION METHODOLOGY FOR  
GRID-BASED DESIGN  
EXAMINER: Tat, Binh C.  
ART UNIT: 2825

**CERTIFICATE OF MAILING**

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Box Non-Fee Amendment, Commissioner for Patents, Washington, DC 20231, on the date printed below:

Date: 4-8-03Name: 

Diane Morse

**AMENDMENT**

BOX NON-FEE AMENDMENT  
COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

Dear Sir:

This paper is in response to the Office Action dated February 27, 2003. Please amend the above-identified application as shown.

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APR 15 2003  
TECHNOLOGY CENTER 2003